

Amendments

IN THE SPECIFICATION:

In paragraphs [0024]-[0026] on page 6 of the specification, please make the following amendments:

Fig. 2 is a circuit diagram illustrating a conventional test mode controller of Fig. 1.

Fig. 3 is a circuit diagram illustrating [[an]] a conventional address decoder of Fig. 1.

Fig. 4 is a circuit diagram illustrating a conventional test mode decoder of Fig. 1.